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"second tapped delay line" summing weighted

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Tap-weight controller for adaptive matched filter receiver ...

In an adaptive matched filter with a decision feedback **equalizer** (DFE), ... a **tap-weight** controller having a **second tapped delay line** of a series of delay ...

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Maximum likelihood estimation using reference and spontaneous ...

A **second tapped-delay line** is formed by delay-line taps 11' to receive ... **tap** signals on the delay line, and an adder for **summing** the **weighted tap** signals. ...

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Fast response matched filter receiver with decision feedback ...

a **second tapped delay line** having a series of delay elements for ... and means for **summing** output signals of said **tap-weight** multipliers to produce a filter ...

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Fast response matched filter receiver with decision feedback ...

A **tap-weight** controller includes a **second tapped delay line** having a series of ... its successive **summing** operations when the demodulator is synchronized. ...

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Equalizer architecture patent invention

An **equalizer** is divided between two **tapped** delay lines. ... An output is then formed as the **weighted sum** of sample values at the sequence of **tap** points. ...

www.freshpatents.com/Equalizer-architecture-dt20050915ptan20050201455.php - 26k -

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Equalizer architecture patent invention

An **equalizer** is divided between two **tapped** delay lines. ... a first **summing** circuit, for forming a first output as a **weighted sum** of sample values from a ...

www.freshpatents.com/Equalizer-architecture-dt20050915ptan20050201455.php?type=claims - 28k - Supplemental Result - [Cached](#) - [Similar pages](#)

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Tap-weight controller for adaptive matched filter receiver ...

a **tap-weight** controller having a **second tapped delay line** of a series of delay ... averaging means for successively **summing** the digital sample from the ...

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Quadriphase shift keyed adaptive equalizer - Patent Review 4053837

The adaptive **equalizer** filter is generally configured in the form of **tapped** delay line transversal filters with each **tap** being **weighted** and the taps being ...

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Phase-directed decision feedback equalizer

The output of decision logic 37 is applied to an output line 20 as well as to the input of a **second tapped delay line** 38 identical to delay line 33. ...

www.emergenthomestore.com/techcat/satellite_retrieval/phase-

[directed_decision_feedback_equalizer.html](#) - 105k - Supplemental Result -
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[Arbitrary waveform modem - Patent # 6396801 - PatentGenius](#)

By controlling the **tap weight** accuracy, the system in accordance with the ... For example, the **weight** values for an **equalizer** or filter can be stored in the ...

[www.patentgenius.com/patent/6396801.html](#) - 69k - Supplemental Result -
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- ☐ 1. [Fast response matched filter receiver with decision feedback equalizer](#)
Yamamoto, Takeshi, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Nov 1994
...having a **tapped** delay line...means for **summing** output signals...feedback **equalizer**.
A tap-weight...includes a **second tapped delay line** having a...feedback **equalizer**;
means for...having a **tapped** delay line...means for **summing** output
signals...**equalizer**; a **second tapped delay line** having a...feedback **equalizer**; a
transversal...having a **tapped** delay line...means for **summing** output
signals...**equalizer**; a **second tapped delay line** having a...
Full text available at patent office. For more in-depth searching go to LexisNexis
[view all 11 results from Patent Offices](#)
[similar results](#)
- ☐ 2. [Quadriphase shift keyed adaptive equalizer](#)
Ryan, Carl R. / Stilwell, James H., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1977
...first and second **tapped** delay lines connected...components and independent
equalizer coefficient generator...each channel. 2. The **equalizer** according to
claim...first and second **summing** means each having...third and fourth **equalizer**
coefficient generators...signal from said first **tapped** delay line and the...signal from said
second tapped delay line; the second inputs...
Full text available at patent office. For more in-depth searching go to LexisNexis
[view all 11 results from Patent Offices](#)
[similar results](#)
- ☐ 3. [Maximum likelihood estimation using reference and spontaneous output peaks of partial response equalizer](#)
Shiokawa, Masato, EUROPEAN PATENT APPLICATION, Mar 1996
...comparison with a prior art **equalizer**; and Fig. 11 shows eye...generally comprises an
adaptive **equalizer** 1, a maximum likelihood...in Fig. 2, the adaptive **equalizer** 1
includes a **tapped**-delay line filter formed...of correlators 12. The **weighted** tap signals
are summed...magnetic recording medium. A **second tapped-delay line** is formed by
delay-line...
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☐ 4. [Arbitrary waveform modem](#)

Upton, Eric L. / Wickham, Michael G., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 2002

...As known in the art, such **tapped** delay lines, for example, formed...said filter architectures, the **tapped** delay lines are known to be...generate a modulated signal, **summing** each of the modulated signals...example, the weight values for an **equalizer** or filter can be stored in...

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☐ 5. [Matched filter for use with a DFE](#)

Yamamoto, Takeshi, EUROPEAN PATENT, Mar 1993

...means for **summing** output signals...feedback **equalizer**. A tap-weight...a **second tapped delay line**...tap of the **second tapped delay line** to produce...feedback **equalizer** (7), wherein...includes a first **tapped** delay line...21) for **summing** output signals...signal of said **equalizer** (7), wherein...includes a **second tapped delay line** (22-26) for...tap of said **second tapped delay line** (22-26) to...

Full text available at patent office. For more in-depth searching go to  LexisNexis

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☐ 6. [DS/CDMA receiver for high-speed fading environment](#)

Ushirokawa, Akihisa / Yoshida, Shosei, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jul 1997

...transversal **equalizer** comprising a **tapped** delay line formed by a series...tap-weight coefficients. The **weighted** signals are summed by an...additionally includes a feedback **equalizer** as illustrated in FIG. 3...multiplier 31 where it is **weighted** by a tap weight coefficient $b_{sub.i}(k)$. After **summing** the **weighted** tap signals by an adder...adds 22 and 32 of the **equalizers** is taken by a subtractor...

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☐ 7. [Tap-weight controller for adaptive matched filter receiver](#)

Yamamoto, Takeshi, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1994

...with a decision feedback **equalizer** according to the present...to a decision feedback **equalizer** 7 which is clocked at...Transversal filter 8 includes a **tapped** delay line formed by a...controller 9 to produce **weighted** digital signals. The outputs...transversal filter has a first **tapped** delay line of a series...which is supplied to an **equalizer**, such as decision feedback...tap-weight controller has a **second tapped delay line** for receiving the digital...

Full text available at patent office. For more in-depth searching go to  LexisNexis


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☐ 8. [Maximal correlation symbol estimation demodulator](#)

Cui, Jian / Hilborn, Derek Stephen, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Aug 2000

...signal is then fed into a **tapped** delay line implemented...of K delay blocks 18. A **second tapped delay line** 19 identical in structure...the art that $F_{sub.R}$ is a **weighted** average of previous correlations...simultaneously, to serve as an **equalizer**, or to provide an enhanced...signal in a conventional **equalizer** or diversity combiner...diversity combining or **equalizer** implementations that use...

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- ☐ 9. [Parallel transmission of TDMA sync and data bursts by artificial frequency selective fade and decision feedback equalization](#)

Tsujimoto, Ichiro, *EUROPEAN PATENT*, Mar 1994

...bursts and the use of a decision feedback **equalizer** at the receive site for extracting the...a spectral null. A decision feedback **equalizer** is connected to the synchronous detector...modulator. Preferably, the decision feedback **equalizer** comprises a feedforward filter comprising...

Full text available at patent office. For more in-depth searching go to  LexisNexis

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- ☐ 10. [Fir filter apparatus for multiplexed processing of time division multiplexed signals](#)

Christopher, Lauren A., *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Feb 1995

...In an embodiment of this type, the **equalizer** and deghoster may be provided a reference...filters. Since such a deghoster and/or **equalizer** are trained with reference to a time...and applied to independent parallel **equalizer** and deghoster circuits, before being...circuitry. FIG. 8 illustrates an output **weighted** FIR filter for processing time division...are coupled to common weighting and **summing** structure. Time division multiplexed...

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- ☐ 11. [FIR filter apparatus for processing of time division multiplexed signals](#)

Christopher, Lauren Ann, *EUROPEAN PATENT*, Feb 1994

...In an embodiment of this type, the **equalizer** and deghoster may be provided a reference...filters. Since such a deghoster and/or **equalizer** are trained with reference to a time...and applied to independent parallel **equalizer** and deghoster circuits, before being...display. FIGURE 8 illustrates an output **weighted** FIR filter for processing time division...

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

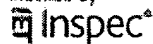
IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Wide-band adaptive antenna nulling using tapped delay lines**
 Mayhan, J.; Simmons, A.; Cummings, W.;
[Antennas and Propagation, IEEE Transactions on \[legacy, pre - 1988\]](#)
 Volume 29, Issue 6, Nov 1981 Page(s):923 - 936
[AbstractPlus](#) | Full Text: [PDF](#)(1384 KB) IEEE JNL
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- ☐ 2. **Field-programmable-gate-array-based time-to-digital converter with 200-p**
 Kalisz, J.; Szplet, R.; Pasierbinski, J.; Poniecki, A.;
[Instrumentation and Measurement, IEEE Transactions on](#)
 Volume 46, Issue 1, Feb. 1997 Page(s):51 - 55
 Digital Object Identifier 10.1109/19.552156
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L11	15	8 and 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:50
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L13	22	(second near (tap\$3 adj delay adj line)) and summ\$3 and weigh\$3 and tap and equaliz\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:11
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L4	4	("4053837" "4504958").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:11
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